



Docket No.: 42390P11446

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

RICHARD COULSON

Application No.: 09/945,266

Filed: August 31, 2001

For: **HARDWARE UPDATED METADATA
FOR NON-VOLATILE MASS STORAGE
CACHE**

Art Group: 2186

Examiner: W. Choi

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

02/22/2005 CCHAU1 00000109 022666 09945266
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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,


BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: February 16, 2005

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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of

Complete if Known

Application Number	09/945,266
Filing Date	August 31, 2001
First Named Inventor	Richard Coulson
Art Unit	2186
Examiner Name	W. Choi
Attorney Docket Number	42390P11446

U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-4430712	02-07-1984	COULSON et al	
		US-4468730	08-28-1984	DODD et al	
		US-4503501	03-05-1985	COULSON et al	
		US-4536836	08-20-1985	DODD et al	
		US-5636355	06-03-1997	RAMAKRISHNAN et al	
		US-5787296	07-28-1998	GRIMSRUD et al	
		US-5809337	09-15-1998	HANNAH et al	
		US-5845313	12-01-1998	ESTAKHRI et al	
		US-5860083	01-12-1999	SUKEGAWA	
		US-5890205	03-30-1999	GRIMSRUD et al	
		US-2002/0160116	10-31-2002	NORDAL et al	
		US-2003/0061436	03-27-2003	ROYER, JR et al	
		US-6370614		TEOMAN et al	
		US-6263405		IRIE et al	
		US-6072490		BATES et al	
		US-5615353	03-25-1997	LAUTZENHEISER	
		US-5269019	12-07-1993	PETERSON et al	
		US-5274799	12-28-1993	BRANT et al	
		US-5442752	08-15-1995	STYCZINSKI	
		US-5499337	03-12-1996	GORDON	

FOREIGN PATENT DOCUMENTS

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Signature**

Date	Considered
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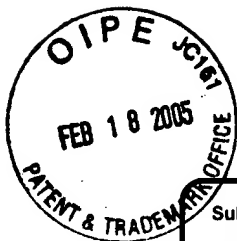
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		Examiner Name	W. Choi
		Attorney Docket Number	42390P11446
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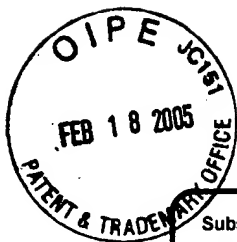
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		COULSON - US PATENT APPLICATION 09/602,008 "IN-LINE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,009 "NON-VOLATILE CACHE EXPANSION BOARD"	
		COULSON - US PATENT APPLICATION 09/602,010 "NON-VOLATILE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,011 "NON-VOLATILE CACHE INTEGRATED WITH MASS STORAGE DEVICE"	
		COULSON - US PATENT APPLICATION 10/740,633 "NON-VOLATILE MASS STORAGE CACHE COHERENCY APPARATUS ADMINISTRATOR"	
		AUCIELLO et al - THE PHYSICS OF FERROELECTRIC MEMORIES, Physics Today, July 1998, pp 22-27	
		SGS THOMSON - GSF32-16x16/90 32 MByte SIMM Glash Memory Module 4 pages	
		DERWENT - DOCUMENT ID RD 431135 A - "A raid controller with a removable NVRAM cache on detecting a battery or an NVRAM fault automatically flushes all...3 pages	
		DEROWITSCH - Smart Computing - "The Quiet Role of Chipsets" Aug. 1998, Vol 9, Issue 8	
		DIEFENDORFF - Intel Tries Integrating Graphics - In-Star MDR Aug. 24, 1998	
		DPT - Smart IV PM2144W PCI High Performance SCSI Adapter	
		DPT - News Release - DPT's SmartCache IV RAID/Caching Kits Outperform Adaptec's SCSI Adapters in Independent Tests and Reviews - Aug. 1, 1996	
		HODGES et al - 1983, pp 388-389 - Analysis and Design of Digital Integrated Circuits	
		INTEL - June 1997 - Intel 430HX PCIsset Design Guide pp 1-1 thru 6-15	

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		INTEL - April 1997 - 82371FB (PIIX) AND 82371SB (PIIX3) PCI ISA IDE XCELERATOR	
		IRE TRANSACTIONS - COMPONENT PARTS - Switching the Memory Matrix	
		MERZ - Sept. 1955 - Ferroelectric Storage Devices pp 335-342	
		MICROSOFT PRESS - Computer Dictionary 2nd Ed. 1994 - Nondestructive readout, page 270	
		MOAZZAMI et al - Oct 1990, Vol. 11, Issue 10 Citation and Abstract IEEE/IEE Electronic Library - A ferroelectric DRAM cell for high-density NVRAMs	
		MUELLER - 2002 - UPGRADING AND REPAIRING PCS - pages 363-366 Chapter 9 - Chipset Evolution	
		NTIS - Dept of Navy - Jan 14, 1974 - MULTI-WIRE CABLE TO COAXIAL CABLE TRANSITION APPARATUS - ADD000450, 8 pages	
		IBM Technical Disclosure Bulletin, Jul. 1982 - OPTICAL/MAGNETIC STORAGE DISK SYSTEM, Vol 25, Issue 2 pages 459-460	
		PULVARI - Mar. 1956, pp 3-11 - FERROELECTRICS AND THEIR MEMORY APPLICATIONS	
		RAMTRON Dec. 7, 1999 - FM24C64 64Kb FRAM Serial Memory - 15 pages	
		RAMTRON Dec. 7, 1999 - FM1608 64Kb Brtewide FRAM Memory - 12 pages	
		RAMTRON Jan 1994 - FRAM Technology - 2 pages	
		ROBERTSON - Sept. 3, 1998 - Hyundai enters Japanese-dominated FeRAM market - 2 pages	
		ROSENBERG - 1987 - Dictionary of Computers, Information Processing & Telecommunications, 2nd Ed. - P. 221	

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